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pipeline and vector processing - two sections devoted completely to the reduced instruction set computer (RISC) - and sample worked-out problems to clarify topics.

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systems and codes, digital circuits, combinational logic design principles and practices, combinational logic design with PLDs, sequential logic design principles and practices, sequential logic design with PLDs, memory, and additional real-world topics (e.g., computer-aided engineering tools, design for testability, estimating digital system reliability, and transmission lines, reflections, and termination). This edition introduces PLDs as soon as possible, emphasizes CMOS logic families and introduces digital circuits in a strongly technology-independent fashion, covers the latest Generic Array Logic (GAL) devices, offers expanded coverage of ROM and RAM system-level design, and provides additional design examples. For those needing a solid introduction or review of the principles and practices of modern digital design. Previously announced in Oct. 1992 PTR Catalogue.

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